

Luca Tornatore

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Education

2005 Ph.D. in Astrophysics; University of Trieste, Italy; Advisor: S. Borgani
1999 Graduation in Physics, University of Padova, Italy; Advisors: O. Pantano, C. Gheller

Working Experience

Since 2017:

Lecturer at the Master in High Performance Computing (MHPC, <https://mhpc.sissa.it/>) at SISSA Trieste.

Lecturer at the the Master degree in Data Science and Scientific Computing (DSSC, <https://dssc.units.it/>) at the University of Trieste.

Since September 2018: staff researcher at the National Institute for Astrophysics, Trieste (Italy)

2016-2018: fixed-term researcher at National Institute for Astrophysics, Trieste (Italy), dedicated at European projectes (ExaNeSt, EuroExa) on HPC and exa-scale

2014-2016: post-doc at National Institute for Astrophysics, Trieste (Italy), dedicated at EUCLID

2012-2013: post-doc at SISSA, Condensed matter sector, focused on QUANTUM-ESPRESSO code

2010-2012: fixed-term researcher at Department of Physics, University Trieste (Italy)

2008-2010: post-doc at the Department of Physics, University of Trieste (Italy)

2006, July: visiting scientist at MPA, Garching (Germany)

2006-2007: post-doc at International School for Advanced Studies (SISSA-ISAS), Astrophysics sector, Trieste

Research Interests

Computational and Theoretical AstroPhysics, N-body simulations, Computational Fluid Dynamics, High performance computing, Cosmology, Space Science.

50+ publications on refereed journals, h-index 31

Abot 20 (non-invited) talks at international conferences, and 3 invited talks

Active participation in 3 Key projects and ~10 ordinary projects at CINECA
PI of 3 ordinary projects at CINECA

Member of INAF-CINECA Time Allocation Committee from 2007 to 2009

Co-Advisor of 3 Phd Thesis and 3 Graduation thesis in Physics (Univ. of Trieste)

Advisor of 2 Master thesis at the Master in High Performance Computing (SISSA/ICTP)

Recent Activity in HPC:

Invited lecturer of HPC weekly course at University of Trento

Talk at eScience (IEEE conference), San Diego, refereed by 3 referees for acceptance

Talk at Future Technology Conference (IEEE conference), San Francisco, refereed by 2 referees for acceptance

Task-leader in ExaNeSt project (ended in 2018 with "Excellent" grade by EU; member of INAF staff that led WP3)

Referee for EuroExaScale-2020 meeting held at HiPeac Conference, Jan 2020